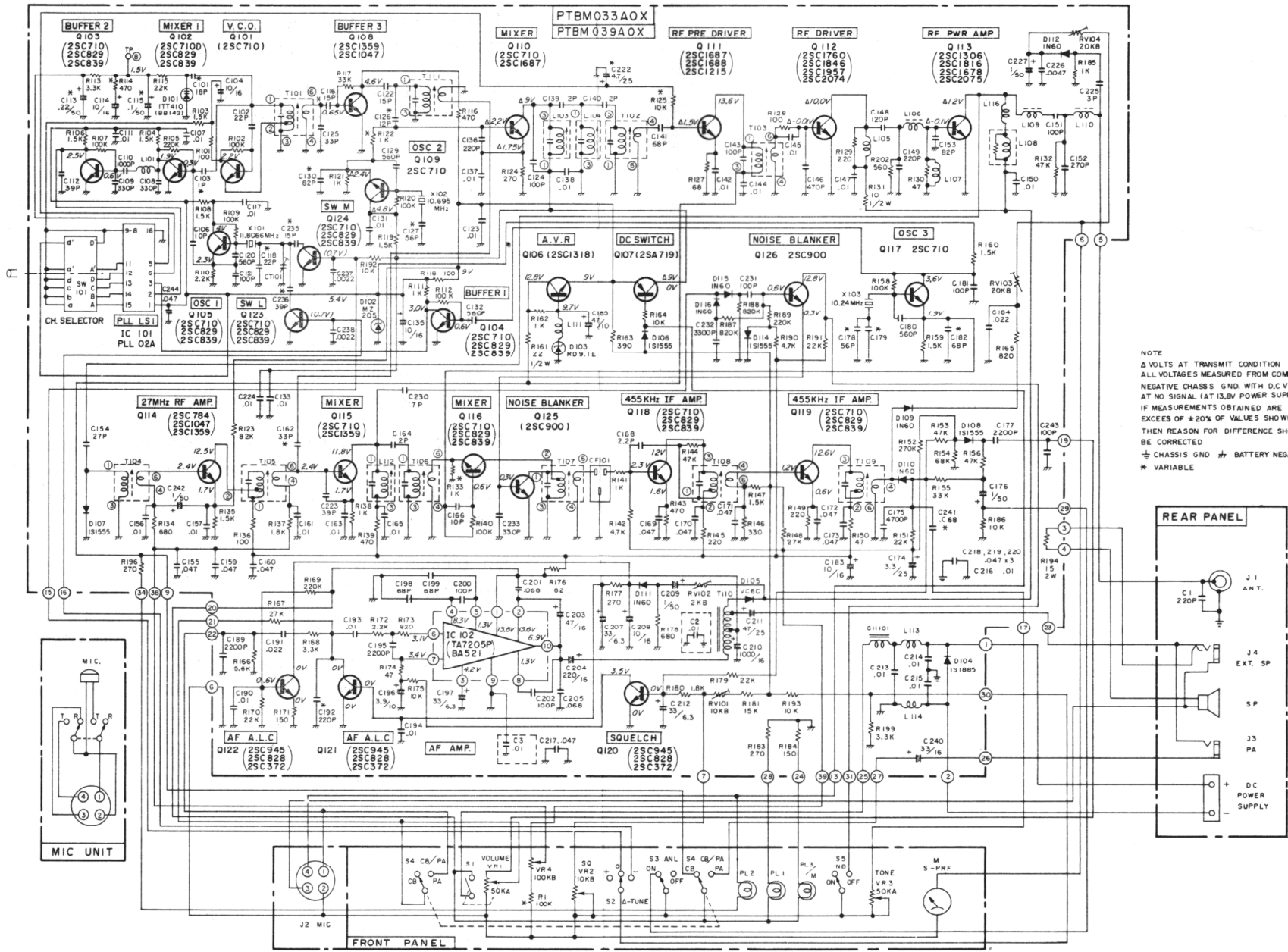
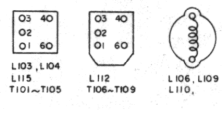


SCHEMATIC DIAGRAM



NOTE
 Δ VOLTS AT TRANSMIT CONDITION
 ALL VOLTAGES MEASURED FROM COMMON
 NEGATIVE CHASSIS GND. WITH D.C. VTM
 AT NO SIGNAL (AT 13.8V POWER SUPPLY)
 IF MEASUREMENTS OBTAINED ARE IN
 EXCESS OF ±20% OF VALUES SHOWN
 THEN REASON FOR DIFFERENCE SHOULD
 BE CORRECTED
 ⊕ CHASSIS GND ⊖ BATTERY NEGATIVE
 * VARIABLE

TRANSFORMER TERMINATION INFORMATION
 BOTTOM VIEW



TRANSISTOR BASE INFORMATION

