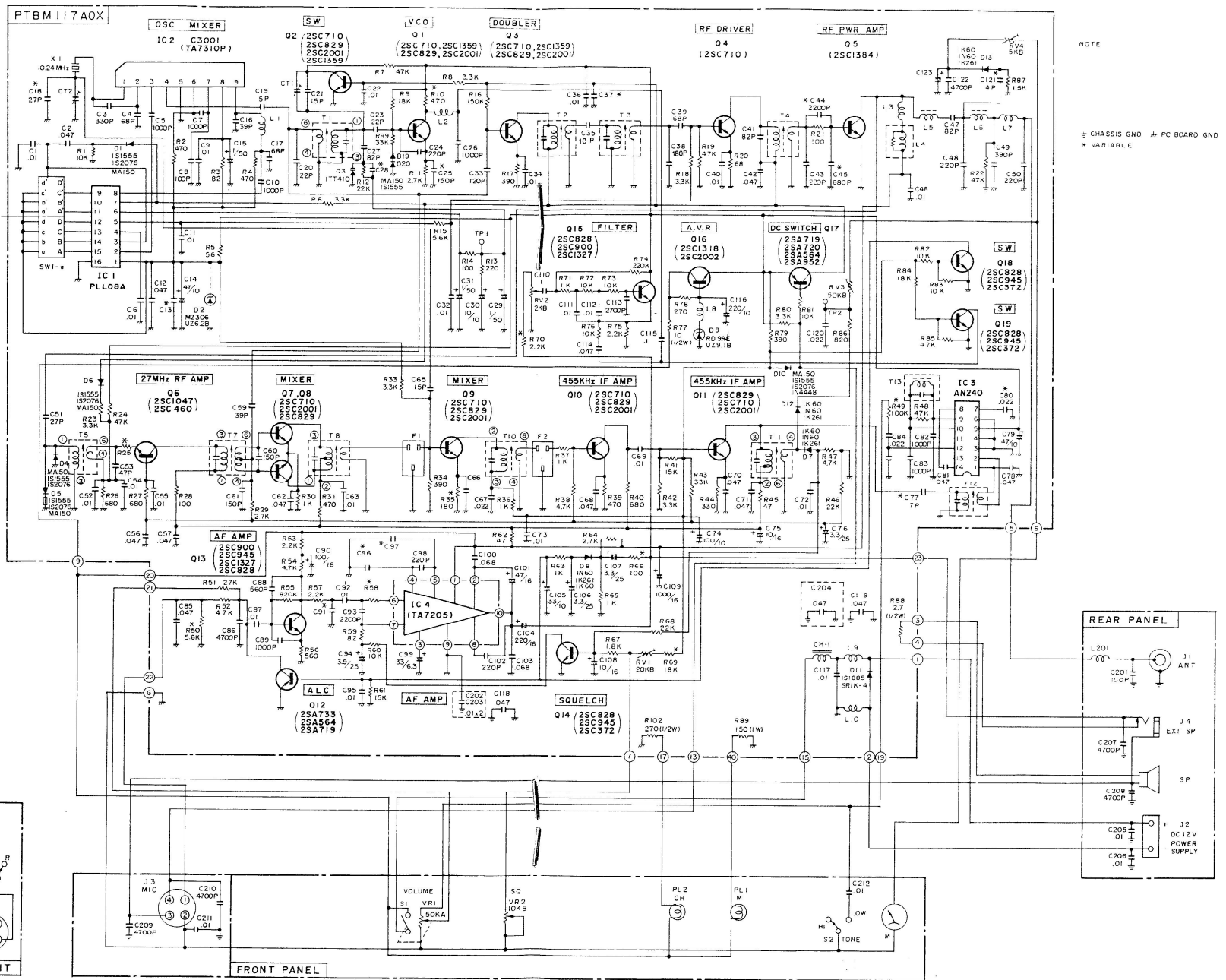
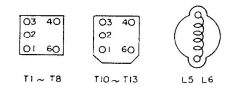


Schema

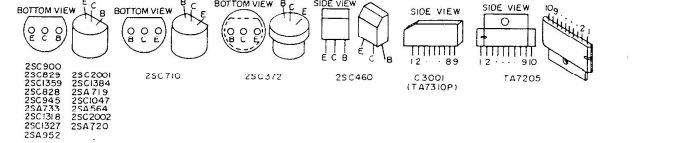


NOTE
 ⊕ CHASSIS GND ⊕ PC BOARD GND
 * VARIABLE

TRANSFORMER TERMINATION INFORMATION (BOTTOM VIEW)



TRANSISTOR BASE INFORMATION E: EMITTER C: COLLECTOR B: BASE



- T1 ~ 78
- T10 ~ 113
- L5 16