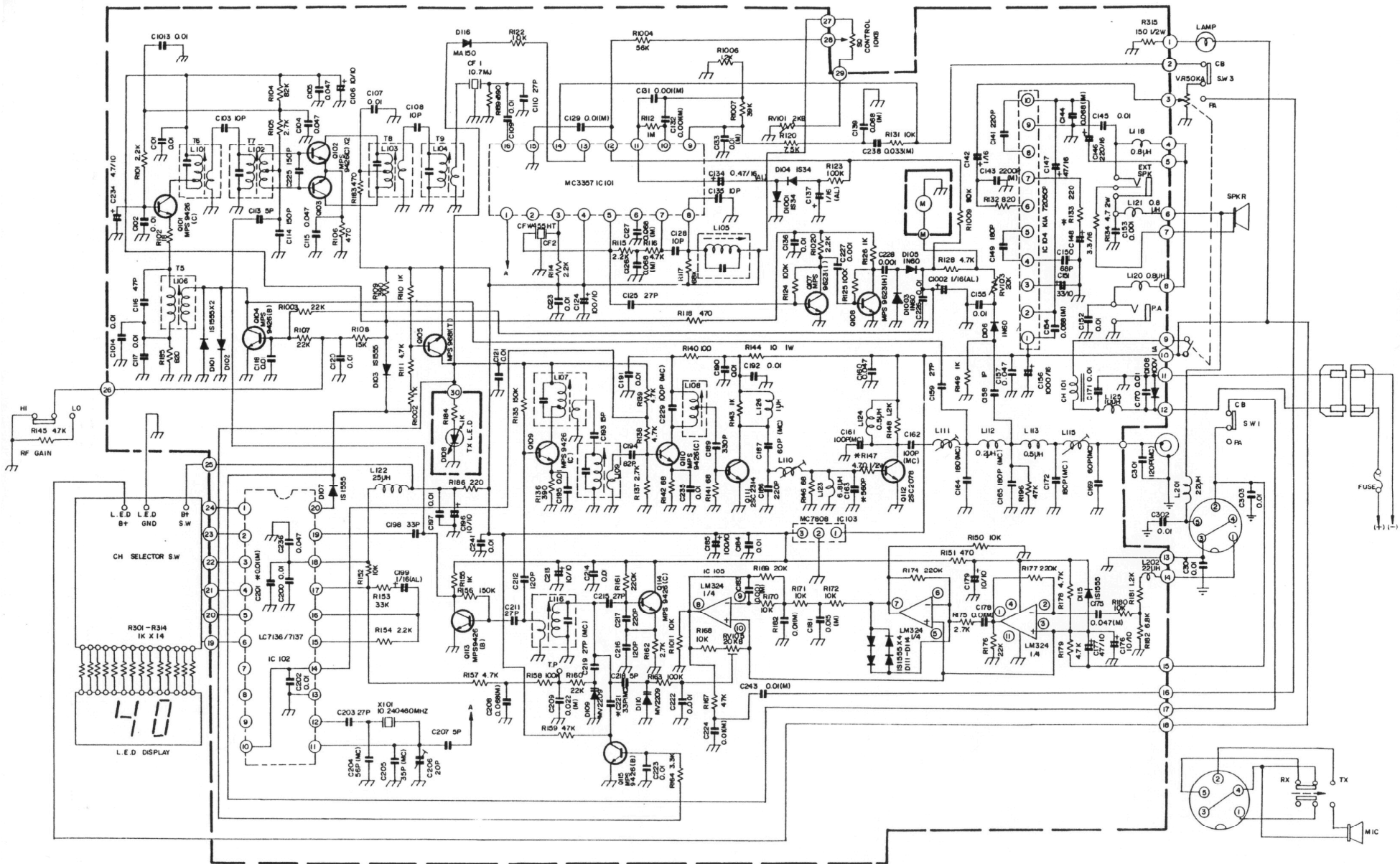
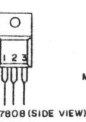
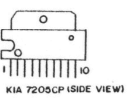
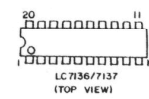
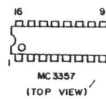
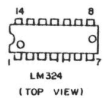


Schematic Diagram.



SEMICONDUCTOR TERMINAL CONNECTION



BOTTOM SIDE VIEW