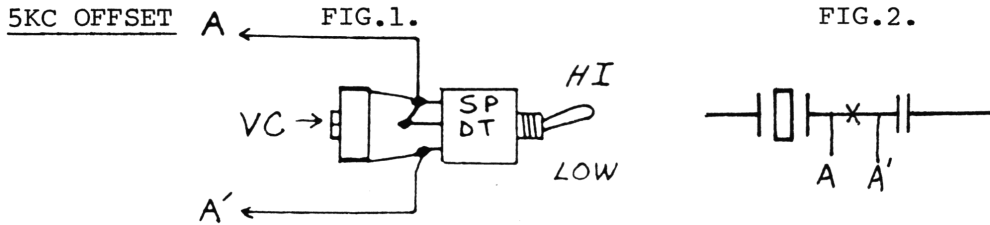


## REALISTIC TRC 422A/421A



1. Wire up the SPDT switch and the trim capacitor as shown in Fig.1.
2. Cut the foil trace between the 10.240MHz. crystal and C37 as shown Fig.2.
3. Solder the wires from the switch to the cut trace.
4. With switch on low position, adjust VC for 27.410 on CH. 40.
5. Switch to high position and check for 27.405. If necessary, alter the value of C37 to obtain this reading.

### CHANNEL CONVERSION

1. Isolate pin 20 of the PLL chip by cutting the foil trace.
2. Solder one leg of the 4700ohm resistor provided to pin 20.
3. Run a wire from the other leg of the resistor to terminal Q on the DPDT switch provided.
4. Run a wire from terminal P on the switch to the unmarked post of the epoxy pak. Also run a wire from terminal P on the switch to the other side of the cut trace (anode of D8).
5. Run a wire from terminal S on the switch to ground.
6. Unsolder and remove C71.
7. Unsolder and remove C41.
8. Solder one leg of the 47pf capacitor supplied to the hole opposite R43/CF2.
9. Run a wire from terminal K on the switch to the other leg of the 47pf capacitor.
10. Run a wire from terminal J on the switch to where the other leg of C41 was connected.
11. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
12. Run a wire from the red dot post of the epoxy pak to pin 18 of the PLL chip.

Now this unit will operate on Channels 42-86, 1-40 and on half channels 1A-40A.

