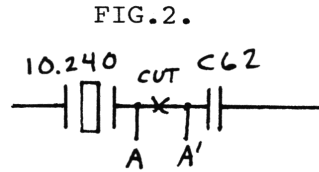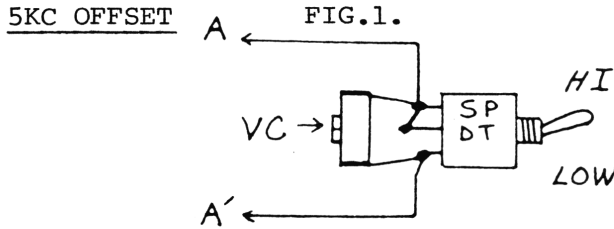# PRESIDENT JAMES K.

5KC OFFSET   FIG.1.   FIG.2.

1. Wire up the SPDT switch and trim capacitor as shown in Fig.1.
2. Cut the foil trace between the 10.240MHz crystal and C62 as shown in Fig.2.
3. Solder the wires from the switch to each side of the cut trace.
4. With the switch on low position, adjust VC for 27.410 on Ch.40.
5. Switch to high position and check for 27.405. If necessary alter the value of C62 to obtain this reading.

CHANNEL CONVERSION

1. Unsolder and lift anode of D17 where it connects to pin 9 of the uPD2814C PLL chip.
2. Solder one leg of the 4700ohm resistor supplied to pin 9 of the PLL chip.
3. Run a wire from the other leg of the resistor to terminal Q on the DPDT switch provided.
4. Run a wire from terminal P on the switch to the lifted end (anode) of D17. Also run a wire from terminal P to the unmarked post of the epoxy pak.
5. Run a wire from terminal S on the switch to ground.
6. Locate, unsolder and remove R93 (off pin 4 of the TA7310 VCO/Mixer chip).
7. Solder one leg of the 47pf capacitor supplied to pin 4 of the TA7310 chip.
8. Run a wire from terminal K on the switch to the other leg of the capacitor.
9. Run a wire from terminal J on the switch to where the other leg of R93 was connected.
10. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
11. Run a wire from the red dot post of the epoxy pak to pin 11 of the PLL chip.

    Now this unit will operate on Channels 42-86,1-40 and on half channels 1A-40A.

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