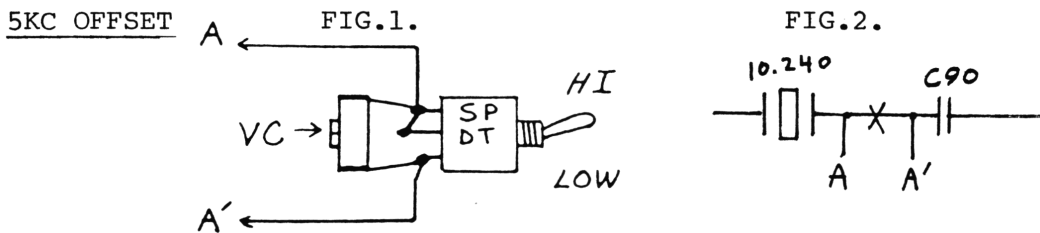


## PRESIDENT "OLD HICKORY"



1. Wire up the SPDT switch and trim capacitor as shown in Fig.1.
2. Unsolder and lift the leg of C90 next to the 10.240 MHz crystal.
3. Solder the wires from the switch to these two points as shown above in Fig.2.
4. With the switch on the low position, adjust VC for 27.410 on Ch.40.
5. Switch to the high position and check for 27.405. You may need to adjust VC1 to obtain this reading.

### CHANNEL CONVERSION

1. Isolate pin 9 of the KM-5624 PLL chip by cutting the foil trace.
2. Solder one leg of the 4700ohm resistor supplied to pin 9.
3. Run a wire from terminal Q on the DPDT switch provided to the other end of the resistor.
4. Run a wire from terminal P on the switch to the other side of the cut trace (anode of D10). Also run a wire from terminal P on the switch to the unmarked post of the epoxy pak.
5. Run a wire from terminal S on the switch to ground.
6. Locate, unsolder and lift the leg of C55 opposite pin 4 of the TA7310 VCO/Mixer chip.
7. Run a wire from the lifted leg of C55 to terminal K on the switch.
8. Run a wire from terminal J on the switch to where C55 was connected.
9. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
10. Run a wire from the red dot post of the epoxy pak to pin 11 of the KM5624 PLL chip.

Now this unit will operate on Channels 42-86, 1-40 and on half channels 1A-40A.

