5KC OFFSET

FIG.1.

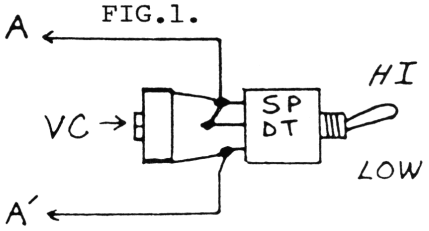
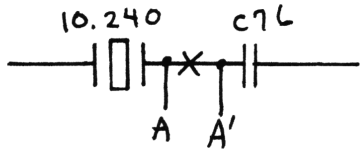


FIG.2.



1. Wire up the SPDT switch and trim capacitor as shown in fig. 1.
2. Cut the foil trace between the 10.240MHz. crystal and C76 as shown in Fig. 2.
3. Solder the wires from the switch to each side of the cut trace.
4. With switch in low position, adjust the VC for 27.410 on Ch. 40.
5. Switch to the high position and check for 27.405. If necessary, alter the value of C76 to compensate.

CHANNEL CONVERSION

1. Unsolder and lift the leg of R58 opposite pin 8 of IC3, the TC9106P PLL chip.
2. Run a wire from terminal Q on the DPDT switch supplied to the lifted leg of R58.
3. Run a wire from terminal P on the switch to where R58 was connected. Also run a wire from terminal P on the switch to the red dot post of the epoxy pak.
4. Run a wire from terminal S on the switch to pin 1 of the TC9106P chip.
5. Locate, unsolder and remove C87 and C88 (off of pin 4 of IC2 TA7310 VCO/Mixer chip.)
6. Solder the leg of the 47pf capacitor supplied to pin 4 of the TA7310P chip.
7. Run a wire from the other leg to terminal K on the switch.
8. Run a wire from terminal J on the switch to where the other side of C88 was connected.
9. Run a wire from terminal L on the switch to the yellow dot post of the epoxy pak.
10. Run a wire from the unmarked post of the epoxy pak to ground.

Now this unit will operate on channels 42-86, 1-40 and on half channels 1A-40A.