5KC OFFSET

FIG. 1.

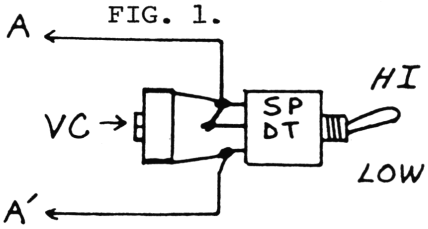
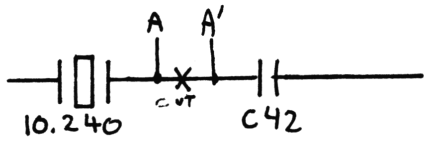


FIG.2.



1. Wire up the SPDT switch and trim capacitor (supplied) as shown in Fig. 1.
2. Cut the foil trace between the 10.240 crystal and C42 and wire as shown in Fig. 2.
3. With switch in low position adjust VC for transmit freq. of 27.410 on Ch. 40.
4. Switch to high position and check for 27.405. If necessary, alter the value of C42 to compensate.

CHANNEL CONVERSION

1. Locate, unsolder, and lift the leg of R47 opposite pin 8 of the TC9106 PLL chip.
2. Run a wire from the lifted leg of R47 to terminal Q on the DPDT switch provided.
3. Run a wire from terminal P on the switch to where R47 was connected. Also run a wire from terminal P to the red dot post of the epoxy pak.
4. Run a wire from terminal S on the switch to pin 1 of the PLL chip.
5. Locate, unsolder, and lift the leg of C136 opposite pin 4 of the TA7310P VCO/Mixer chip.
6. Run a wire from the lifted leg of C136 to terminal K on the switch.
7. Run a wire from terminal J to where the other leg of C136 was connected.
8. Run a wire from terminal L to the yellow dot post of the epoxy pak.
9. Run a wire from the unmarked post of the epoxy pak to ground.

Now this unit will operate on Channels 42-86,1-40 and on half channels 1A-40A.