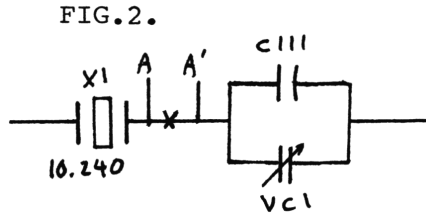
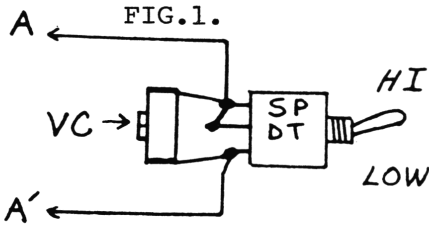


5KC OFFSET



1. Wire up the SPDT switch and the trimmer capacitor (supplied) as shown in Fig.1.
2. Cut the trace between the 10.240MHz. crystal and VC1/C111. Solder the wires from the switch to each side of the cut trace as shown in Fig.2.
3. With switch in low position, adjust VC for 27.410 MHz on Ch. 40.
4. Switch to high position and check for 27.405. If necessary, adjust VC1 or change C111 to obtain this reading.

CHANNEL CONVERSION

1. Unsolder and lift the end of R58 opposite pin 8 of IC3.
2. Run a wire from terminal Q of the DPDT switch to the lifted end of R58.
3. Run a wire from terminal P to where R58 was connected. Also run a wire from P to the red dot post of the epoxy pak. *ALSO Run wire from L to Yel Dot on Epoxy Pak*
4. Run a wire from terminal S to pin 1 of the PLL chip.
5. Unsolder and remove R69 (off pin 4 of IC2).
6. Solder one end of the 47pf capacitor supplied to pin 4.
7. Connect the other leg of the 47pf capacitor to terminal K of the switch.
8. Run a wire from terminal J to where R69 was connected opposite pin 4.
9. Run a wire from the unmarked post of the epoxy pak to ground.

Now this unit will operate on Channels 42-86, 1-40 and on half channels 1A-40A.

