

PLL PINOUT DIAGRAM (CONT)

TC9106P

This monster uses a read-only memory, derived from the LED 7 segment code. 40 channels only.

1. V+.
2. Ref OSC input.
3. No conection.
4. Lock detector.
5. NC.
6. NC.
7. Phase detector.
8. Transmit/Receive IF shift.
9. Prog. Div. input
10. P⁰.
11. P1.
12. P2.
13. P3.
14. P4.
15. P5.
16. P6.
17. P7.
18. Ground.

TC9109P

Same as above except it has on chip loop filter input and output, pins 5, and 6.