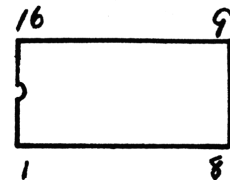


## PLL PINOUT DIAGRAMS (CONT)

### PLL 02A

Pin	1	Voltage Supply	
	2	Prog. Div. Input	
	3	Ref. Freq. Input	
	4	Ref Dev. Output(10KC)	
	5	Output of Charge Pump	
	6	Lock Detector Output-Normally high (Low-Xmit stop)	
	7	Program Input	P8
	8		P7
	9		P6
	10		P5
	11		P4
	12		P3
	13		P2
	14		P1
	15		P0
	16	Ground	



### TC 5080P

Pin	1	Prog. input	P0 (LSB)
	2		P1
	3		P2
	4		P3
	5		P4
	6		P5
	7		P6
	8		P7 (MSB)
	9	Ground	
	10	Output of programmable divider	
	11	Prog. code select pin	
	12	RESET (Low-counts; High-cleared)	
	13	High- counter enabled; Low- disabled	
	14	No connection	
	15	Input to prog. divider	
	16	Supply voltage 4.5-8.0Volts	

